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| 2nd Supplemental Form PTO-1449 (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary) | | ATTY. DOCKET NO. AMDI:133/HON | SERIAL NO. 10/790,939 |
| | | APPLICANT Akif Sultan et al. | CERTIFICATE OF FACSIMILE TRANSMISSION 37 C.F.R. § 1.6(d) I hereby certify that this correspondence is being transmitted by facsimile to the Patent and Trademark Office (Fax No. (703) 872-9308), on the date below: Date of transmission: <u>February 26, 2003</u> Signature: <u>[Signature]</u> Printed Name: <u>Timothy M. Honeycutt</u> |
| | | FILING DATE March 1, 2004 | GROUP 2812 |

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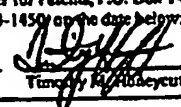
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| EXAMINER <u>Quoc Hoang</u> | DATE CONSIDERED <u>03/2006</u> |
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| | | APPLICANT Akif Sultan et al. | CERTIFICATE OF MAILING 37 C.F.R. 1.8 I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on the date below: MAY 21, 2004 Date:  Timothy R. Riney |
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| EXAM INIT. | REF. DES. | DOCUMENT NUMBER | DATE | NAME | CLASS | SUB CLASS | FILING DATE IF APPROPRIATE |
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| <i>m</i> | A1 | 6,114,211 | September 5, 2000 | H. Jim Fulford et al. | 448 | 305 | — |
| <i>m</i> | A2 | 5,227,321 | July 13, 1993 | Ruojia Lee et al. | 437 | 44 | — |
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| <i>m</i> | A4 | Stanley Wolf and Richard N. Tauber, <i>Silicon Processing for the VLSI Era, Volume 3 - The Submicron MOSFET</i> , pp.226-232, 240, 309-311 and 621-622; 1995 |
| <i>m</i> | A5 | Terence B. Hook et al.; <i>High-Performance Logic and High-Gain Analog CMOS Transistors Formed by a Shadow-Mask Technique With a Single Implant Step</i> ; IEEE Transactions on Electron Devices, Vol. 49, No. 9, September 2002; pp. 1623-1627 |

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| EXAMINER <i>Wu Hwang</i> | DATE CONSIDERED <i>03/20/06</i> |
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| m | A1 | 6,114,211 | September 5, 2000 | H. Jim Fulford et al. | 448 | 305 | — |
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| m | A5 | Terence B. Hook et al.; <i>High-Performance Logic and High-Gain Analog CMOS Transistors Formed by a Shadow-Mask Technique With a Single Implant Step</i> ; IEEE Transactions on Electron Devices, Vol. 49, No. 9, September 2002; pp. 1623-1627 |

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| | | APPLICANT Akif Sultan et al. | | Filed via EFS-Web | | | |
| | | FILING DATE March 1, 2004 | | GROUP 2818 | | | |
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